



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: CHAUG, et al

Application No.: 10/665,992

Filed: September 19, 2003

For: PROCESS FOR FORMING A
PATTERNED THIN FILM STRUCTURE
FOR IN-MOLD DECORATION

Atty. Docket No.: SIPXP101

Examiner: Unassigned

Group: Unassigned

Date: October 7, 2003

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop _____, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on October 7, 2003.

Signed: Pat Tate
Pat Tate

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§1.56 AND 1.97(b) BEFORE MAILING OF A FIRST OFFICE ACTION

Mail Stop _____
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a continuation-in-part of prior application U.S. Patent Application No. 10/422,557.

This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed within three (3) months of the filing date of the above-referenced application. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0685 (Order No. SIPXP101).

Respectfully submitted,

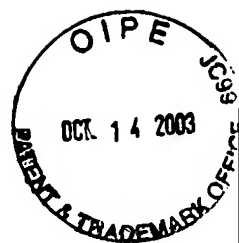
VAN PELT & YI LLP



William J. James

Reg. No. 40,661

10050 N. Foothill Blvd, Ste 200
Cupertino, CA 95014
(408) 973-2585



Form 1449 (Modified)	Atty Docket No. SIPXP101
Information Disclosure Statement By Applicant	Application No.: 10/665,992
	Inventor CHAUG, et al
	Group Unassigned
(Use Several Sheets if Necessary)	Filing Date September 19, 2003

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	3,612,758	10/12/71	Evans, et al	178	5.4R	10/3/69
	B	3,668,106	6/6/72	Ota	204	299	4/9/70
	C	3,697,679	10/10/72	Hathaway	178	6.6A	7/1/70
	D	4,071,430	1/31/78	Liebert	204	299	12/6/76
	E	4,093,534	6/6/78	Carter, et al	350	355	5/5/75
	F	4,285,801	8/25/81	Chiang	204	299 R	8/20/79
	G	4,655,897	4/7/87	DiSanto et al	204	299 R	11/13/84
	H	4,663,192	5/5/87	Hatakeyama	427	108	
	I	4,680,103	7/14/87	Beilin Solomon I., et al	204	299 R	1/24/86
	J	4,741,988	5/3/88	Van der Zande, et al	430	312	5/1/86
	K	4,995,718	2/26/91	Jachimowicz et al	353	31	11/15/89
	L	5,102,497	4/7/92	Hamaguchi et al	156	656	
	M	5,177,476	1/5/93	DiSanto, et al	345	107	8/19/91
	N	5,276,438	1/4/94	DiSanto, et al	345	107	11/20/91
	O	5,279,511	1/18/94	DiSanto, et al	445	24	10/21/92
	P	5,380,362	1/10/95	Schubert	106	493	7/16/93

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	Q	WO 99/56171	11/4/99	PCT	G02F	1/167		
	R	WO 00/60410	10/12/00	PCT	G02F	1/167		
	S	WO 01/67170	9/13/01	PCT	G02F	1/167		
	T	WO 00/36465	6/22/00	PCT	G02F	1/167		
	U	1 089 117 A2	4/4/01	Europe	G02F	1/167		
	V	59-171930	9/28/84	Japan	G02F	1/19		

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Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	W	Cominsky, B. et al., "An Electrophoretic Ink for All-Printed Reflective Electronic Displays", Letters to Nature, pp. 253-255 (1998)
	X	Dalisa, A. L., "Electrophoretic Display Technology", IEEE Transactions of Electron Devices, July 1997, pp. 827-834.
	Y	Drzaic, PI, "Liquid Crystal Dispersion", The PDLC Paradigm, (1995) p. 1-9
	Z	Harvey, T. G. "Replication Techniques for Micro-Optics", SPIE, Vol 3099, p 76-82 (1997)
	AA	Hopper, M.A. and Novotny, V., "An Electrophoretic Display, It's Properties, Model and Addressing" IEEE Transactions on Electron Devices, Vol. ED-26, No. 8, Aug. 1979, pp. 1148-1152

Examiner _____ Date Considered _____
 Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. SIPXP101 Application No.: 10/665,992 Inventor CHAUG, et al Group Unassigned Filing Date September 19, 2003
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	5,403,518	4/4/95	Schubert	252	572	12/2/93
	B	5,573,711	11/12/96	Hou, et al	252	572	11/21/95
	C	5,589,100	12/31/96	Grasso	252	299.01	3/20/96
	D	5,699,097	12/16/97	Takayama, et al	347	171	4/20/95
	E	5,835,174	11/10/98	Clinkeman, et al	349	86	9/19/96
	F	5,914,806	6/22/99	Gordon II, et al	359	296	2/11/98
	G	5,930,026	7/27/99	Jacobson, et al	359	296	10/25/96
	H	5,961,804	10/5/99	Jacobson, et al	204	606	3/18/97
	I	5,976,405	11/2/99	Clinkeman, et al	252	299.01	12/23/97
	J	6,017,584	1/25/00	Albert, et al	427	213.3	8/27/98
	K	6,037,058	3/14/00	Clinkeman, et al	428	402.2	5/26/99
	L	6,067,185	5/23/00	Albert, et al	359	296	8/27/98
	M	6,111,598	8/29/00	Faris	348	57	11/12/93
	N	6,113,810	9/5/00	Hou, et al	252	572	3/3/95
	O	6,120,588	9/19/00	Jacobson	106	31.16	9/23/97
	P	6,120,839	9/19/00	Comiskey, et al	427	213.3	8/27/98
	Q	6,172,798	1/9/01	Albert, et al	359	296	5/15/00
	R	6,184,856	2/6/01	Gordon, II, et al	345	107	9/16/98
	S	6,232,950	5/15/01	Albert et al	345	107	8/27/98
	T	6,252,624	6/26/01	Yuasa, et al	348	56	3/12/98
	U	6,312,304	11/6/01	Duthaler, et al	445	24	12/14/99
	V	6,327,072	12/4/01	Comiskey, et al	359	296	4/6/00
	W	6,337,761	1/8/02	Rogers, et al	359	296	10/1/99
	X	6,392,786	5/21/02	Albert	359	296	6/29/00
	Y	20020126249	9/12/02	Liang, et al	349	187	1/11/01
	Z	20020182544	12/5/02	Chan-Park, et al	430	311	1/15/01

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	AA	Harbour, J.R. et al, "Subdivided Electrophoretic Display", Xerox Disclosure Journal, Vol 4, No. 6 (1979)
	BB	Inoue, et al, "Low Temperature Poly-Si TFT – Electrophoretic Displays (TFT-EPDs) with Four Level Gray Scale", Int'l Electron Devices Meeting 2000, Technical Digest San Francisco, CA Dec 10-13, 2000, pages 197-200.
	CC	Lewis, J.C., et al., "Gravitational, Inter-Particle and Particle-Electrode Forces in Electrophoretic Display" Proceeding of the S.I.D., Vol 18/3&4 1977.
	DD	
Examiner		Date Considered

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	A	09/518,488	3/3/00	LIANG, et al			
	B	09/606,654	6/28/00	LIANG, et al			

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C	Murau, P., et al., "The Understanding and Elimination of Some Suspension Instabilities in an Electrophoretic Display", J. Appl. Phys., 49 (9), (1978)
	D	Nakamura, E., et al., "Development of Electrophoretic Display Using Microcapsulated Suspension", SID Digest, (1998), pp. 1014-1017.
	E	Ota, I., et al., "Electrophoretic Image Display EPID Panel", Proceedings of the IEEE, Vol 1, No. 7, July 1973
	F	Singer, B. et al, "An X-Y Addressable Electrophoretic Display", Proceeding of the S.I.D., Vol 18/3&\$, (1977)
	G	Slafer, D.W., et al., "Continuous Manufacturing of Thin Cover Sheet Optical Media", SPIE, Vol. 1663, (1992), pp. 323-335.
Examiner		Date Considered

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